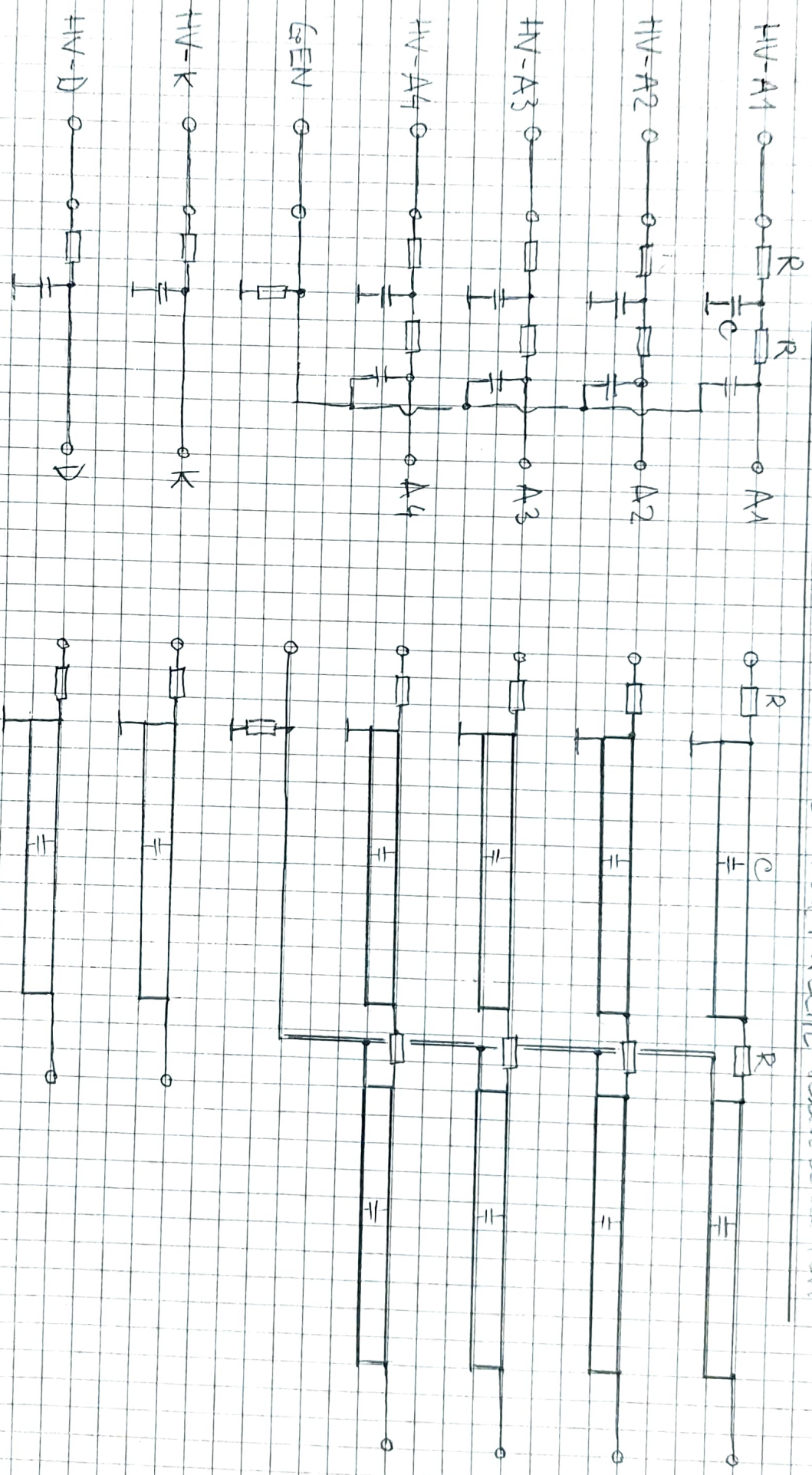


FILTRE HV (H, K, D) + CIRCUIT de INTERCIE pentru cel de test



Schema de principiu

Dispozitive pe PCB